



VERIFICATION & VALIDATION TEST REPORT

SPI / FRAM Verification Test

1. Document Information

Document Number	VV-SPI-001
Document Revision	A
Report Version	1.0
Prepared By	Jack
Reviewed By	-----
Report Status	DRAFT

2. Project Information

Session ID	SPI-2026-09-22_10-05-10
Project Name	V&V Automation Test Practice Demo
Test Suite	SPI / FRAM Verification Test



Device Under Test	STM32 SPI FRAM Demo
DUT Serial Number	DEMO-DUT-001
Firmware Version	1.0.0
Operator	Jack
Session Start	2026-09-22 10:05:10
Session End	2026-09-22 10:05:16
Duration	00:00:06

3. Test Summary

6 Total Tests	5 Passed	1 Failed	83.3% Pass Rate
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Overall Session Result	FAIL
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4. Detailed Test Results

4.1 SPI / FRAM Verification Tests



Time	Test Case	Test Name	Expected	Actual	Device State	Test Status	Result
09/22/2026 10:05:11 AM	SPI_001	Unique Device ID	Valid 64-bit Unique ID	00 00 00 00 00 00 00 00	READY	SPI_TEST_STATUS_ PASS	FAIL
09/22/2026 10:05:12 AM	SPI_002	Write Enable	WEL bit = 1	0x42 (WEL=1)	READY	SPI_TEST_STATUS_ PASS	PASS
09/22/2026 10:05:13 AM	SPI_003	Single Byte Read/ Write	0x5A	0x5A	READY	SPI_TEST_STATUS_ PASS	PASS
09/22/2026 10:05:14 AM	SPI_004	Multiple Byte Read/ Write	11 22 33 44 55 66 77 88	11 22 33 44 55 66 77 88	READY	SPI_TEST_STATUS_ PASS	PASS
09/22/2026 10:05:15 AM	SPI_005	SPI Comm. Error Handling	Comm. error detected	COMM_ERROR	COMM_ERROR	SPI_TEST_STATUS_ FAIL	PASS
09/22/2026 10:05:16 AM	SPI_006	SPI No-Hang	NO HANG	NO HANG	READY	SPI_TEST_STATUS_ PASS	PASS

5. Failure Details

SPI_001 - FAIL

Invalid SPI FRAM unique device ID. Received = 00 00 00 00 00 00 00 00.